

Last time we considered the requirements for fault-tolerant quantum gates that act nontrivially on the codespace of a quantum error-correcting code. In the special case of a code that corrects $t=1$ error, the requirements are:

- if the gate gadget is ideal (has no faults) and its input is a codeword, then the gadget realizes the encoded operation U acting on the code space.
- if the gate gadget is ideal and its input has at most one error (is one-deviated from the codespace), then the output has at most one error in each output block.
- if the gate has one fault and its input has no errors, then the output has at most one error in each block (the errors are correctable).

We considered the Clifford group, the finite subgroup of the m -qubit unitary group generated by the Hadamard gate H , the phase gate P (rotation by $\pi/2$ about the z -axis) and the CNOT gate. For a special class of codes, the generators of the Clifford group can be executed *transversally* (i.e., bitwise). The logical U can be done by applying a product of n U (or inverse of U) gates in parallel (where n is the code's length). If we suppose that the number of encoded qubits is $k=1$, then:

- the CNOT gate is transversal for any CSS code.
- the H gate is transversal for a CSS code that uses the same classical code to correct X errors and Z errors.
- the P gate is transversal if the stabilizer generators have weight a multiple of 4, and the logical Pauli operators X and Z have weight $\equiv 1 \pmod{4}$, or have weight $\equiv 3 \pmod{4}$. (In the latter case we do the transversal inverse of P to execute the logical P .)

In particular, Steane's $[[7,1,3]]$ code has all of these properties, so we can do Clifford group computation transversally for that code. Transversal operations are fault tolerant: they don't propagate errors from one qubit in a block to another qubit in the same block, and a single faulty gate damages at most one qubit in each block.

Of course, the Clifford group is discrete so that the Clifford generators are not a universal gate set for quantum computing; in fact Clifford group computation can be simulated efficiently on a classical computer. So we need to consider how to augment our fault-tolerant Clifford gates with another gate that completes a universal set. But before we do that, let's generalize the observation that we can do Clifford group computation fault tolerantly. We will show this is possible for any stabilizer code.

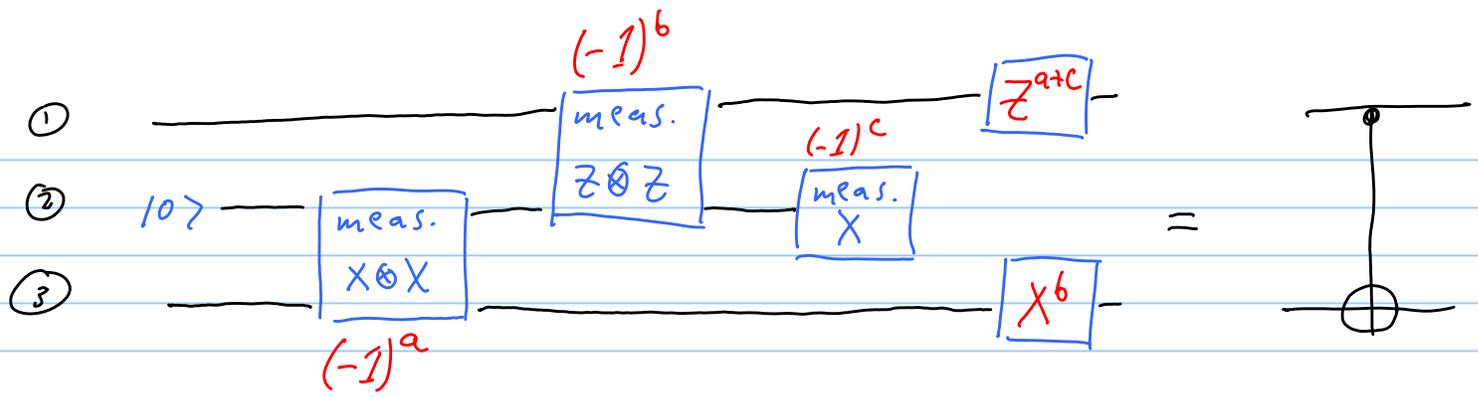
Specifically, we will show (an observation that has useful applications even beyond the study of fault-tolerance): The following operations suffice for realizing the Clifford group:

- preparing the Z eigenstate $|0\rangle$.
- applying the Pauli operators X, Z to any qubit.
- measuring weight-1 Pauli operators X, Y and measuring weight-two Pauli operators XX, ZZ, ZX .

This observation is useful in the study of fault tolerance because, for any stabilizer code, any logical Pauli operator can be realized as a Pauli operator (a tensor product of Pauli matrices) which is fault tolerant (r faults cause at most r errors in the block). Furthermore, we have seen that Pauli operators can be measured fault tolerantly, e.g. by using the cat-state method, repeating measurements, and doing majority voting on the observed outcomes. This is true even for the measurement of the tensor product of two logical Pauli operators in the same code block, since the weight-two logical Pauli operator is also just a tensor product of Pauli operators acting on qubits in the block.

As we already saw last time, since Clifford gates acting by conjugation take Pauli operators to Pauli operators, it is quite convenient to describe these gates in the "Heisenberg picture" -- i.e., in terms of their action on operators rather than states.

First consider execution of the CNOT gate. It can be achieved by the circuit:



Here $(-1)^a$ denotes the outcome of meas of $X \otimes X$, etc., and Z^{a+c} means apply Z conditioned on parity of outcomes of $X \otimes X$ and X meas.

How do we see that it works? We recall the action of CNOT (by conjugation) on Pauli ops.

$$\text{CNOT:} \quad \begin{array}{ll} XI \rightarrow XX & ZI \rightarrow ZI \\ IX \rightarrow IX & IZ \rightarrow ZZ \end{array}$$

control \rightarrow target

We'll consider how X_i, X_3, Z_1, Z_3 propagate through the circuit (where X_i denotes qubit # i). We'll also keep track of how the stabilizer of the 3-qubit state evolves as the circuit is executed.

Initially, the stabilizer is $M = IZI$, because the middle qubit is prepared in the state $Z = +I$. Then we measure IXX , obtaining outcome $(-1)^a$. This measurement commutes with

$$\begin{array}{ll} X_1 = XI I & X_3 = I I X \\ Z_1 = Z I I & \text{but not with } Z_3 = I I Z \end{array}$$

However, because $Z_2 = I$, we are free to replace

$$Z_3 = I I Z \rightarrow I Z Z,$$

which does commute with IXX

The next step is to measure ZZI , which does not commute with

$X_1 = X I I$. But after the first measurement, the stabilizer of the 3-qubit state has been transformed to $(-1)^a I X X$. Therefore we may make the replacement

$$X_1 = X I I \rightarrow (-1)^a X X X,$$

which does commute with $Z I I$. Now in the last step we measure $I X I$, which does not commute with $Z_3 = I Z Z$. But after the 2nd meas., the stabilizer is $(-1)^b Z I Z$, so we replace

$$Z_3 = I Z Z \rightarrow (-1)^b Z I Z,$$

which does commute with $I X I$. Using the final stabilizer $(-1)^{a+c} I X I$, we find the Pauli operators on qubits 1 and 3 have been transformed as:

$$X_1 \rightarrow (-1)^a X X X \rightarrow (-1)^{a+c} X I X$$

$$Z_1 \rightarrow Z I I$$

$$X_3 \rightarrow I I X$$

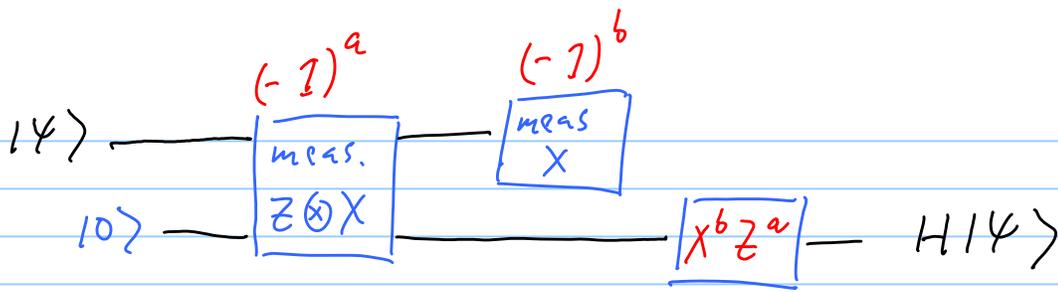
$$Z_3 \rightarrow (-1)^b Z I Z$$

This is the action of CNOT with control 1 and target 3, except for the minus signs.

We can flip the sign of X_1 (not affecting X_3) by applying Z_1 , if $a+c = 1 \pmod{2}$. We can flip the sign of Z_3 (without affecting Z_1) by apply X_3 , if $b = 1$. That completes the CNOT gate.

And ... if the state preparations and measurements can be done fault tolerantly, so can the CNOT gate, if we insert an error correction state after each preparation or measurement. Furthermore, we can apply a CNOT gate from any encoded qubit in the control block to any encoded qubit in the target block, as long as we can measure the corresponding weight-two logical Pauli operators.

Next, a circuit for the Hadamard gate H:



Note that in this case the "ancilla" and data switch places.

To check that it works, recall action of H by conjugation:

$$H: X \leftrightarrow Z$$

Initially the stabilizer is $M = IZ$. How do $X_1 = XI$ and $Z_1 = ZI$ propagate through the circuit?

First we measure ZX , which does not commute with X_1 . But using the stabilizer

$$X_1 = XI \rightarrow XZ \quad \text{which does commute with } ZX$$

Then we measure XI , which does not commute with $Z_1 = ZI$. But the stabilizer after the first meas. is $(-1)^a ZX$, so we may replace

$$Z_1 = ZI \rightarrow (-1)^a IX.$$

After the 2nd meas., then, the Pauli ops acting on the input qubit have been transformed as

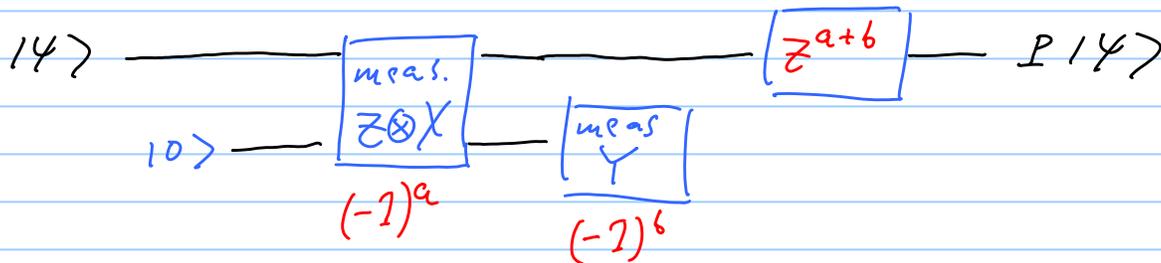
$$\begin{aligned} X_1 &\rightarrow XZ = (-1)^b IZ \\ Z_1 &\rightarrow (-1)^a IX \end{aligned}$$

The quantum info initially carried by qubit 1 is transferred to qubit 2, but with a nontrivial operation applied.

We can get rid of the minus sign by applying X if $b=1$ and applying Z if $a=1$.

Finally, we need a circuit for

$$P = \begin{pmatrix} 1 & 0 \\ 0 & i \end{pmatrix}; \quad \begin{array}{l} Z \rightarrow Z \\ X \rightarrow PX P^{-1} = Y \end{array} \quad (\text{a } \pi/2 \text{ rotation in } x\text{-}y \text{ plane})$$



Here the operations commute with $Z_1 = Z I$, so clearly $Z_1 \rightarrow Z_1$. But how does $X_1 = X I$ propagate through the circuit? The initial stabilizer is $M = I Z$, and first meas. is $Z \otimes X$, as for the Hadamard circuit, so that $X_1 \rightarrow X Z$ in the first step.

Since $X Z$ does not commute with Y meas, we use the new stabilizer $(-1)^a Z X$ to replace

$$X_1 = X Z \rightarrow (-1)^a (Z X)(X Z) = (-1)^a Y Y$$

Then, when we measure Y_2 , we have

$$X_1 = (-1)^a Y Y \rightarrow (-1)^{a+b} Y I$$

We can remove the minus sign by applying Z (which commutes with Z_1) $a+b \pmod{2} = I$.

Thus we have the P gate: $X \rightarrow Y$

Now we have seen how to apply fault-tolerant Clifford group gates for any stabilizer code (since we can do Pauli operators, Pauli operator eigenstate preparations and Pauli operator measurements fault-tolerantly).

But how do we go beyond the Clifford group and complete a universal fault-tolerant logical gate set? One thing we should recognize is that we cannot expect to be able to do all the logical gates in a universal set transversally. If the transversal operations that preserve the code space are actually universal, then these operations surely are a continuous set, so we can consider the infinitesimal operations in this set --- those that are very close to the identity operation. Because a transversal operation acting on a length- n codeblock is a product of n operations, each of which acts on only one qubit in the codeblock, an infinitesimal transversal operation V has the form

$$V = I + \epsilon (A_1 + A_2 + \dots + A_n)$$

where A_i acts nontrivially only on qubit # i in the block. Since this is a logical operation, V preserves the codespace

$$V\Pi = \Pi V\Pi \quad \text{where } \Pi \text{ is the projector onto the codespace}$$

$$\text{Thus } (\sum A_i)\Pi = \Pi(\sum A_i)\Pi$$

We also know that for a code that can detect weight-one errors

$$\Pi E \Pi \propto \Pi \quad (\text{where } E \text{ has weight one})$$

- if we successfully project onto the codespace the error E is reversed. Since each A_i has $wt=1$

$$\Pi A_i \Pi \propto \Pi \quad \text{for each } i$$

$$\text{therefore } (\sum A_i)\Pi = \Pi(\sum A_i)\Pi \propto \Pi$$

and the operation V acting on the code space is the identity. We conclude that continuously varying logical operation cannot be done transversally --- at best we are limited to a discrete set. The problem is that if we could do a logical operation by applying infinitesimal unitaries to each individual qubit in the block, we would be unable to protect the encoded state from small unitary errors acting on qubits.

To complete the universal logical gate set, we will instead borrow the idea we have used to make error correction fault tolerant, and also to realize Clifford gates for arbitrary stabilizer codes. We prepare "offline" a special encoded ancilla state that can be verified, then use joint measurement on the data block and the ancilla to realize the desired gate. The principle behind this strategy is that it is easier to verify a known quantum state than to certify that a known unitary transformation has been properly applied to an unknown state. If the verification of the state fails, we can either repair the state or discard it and attempt to prepare again, without endangering the data.



When we think about how to complete the fault-tolerant gate set, it is useful to keep in mind a hierarchical classification of unitary gates --- the C_r classification.

C_1 is the Pauli group

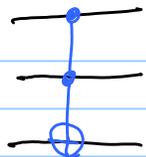
C_2 is the Clifford group: the unitaries whose action by conjugation maps a Pauli operator to a Pauli operator

C_3 gates acting by conjugation map Pauli operators to C_2 (Clifford) operators

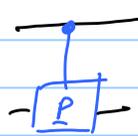
C_r gates acting by conjugation map Pauli operators to C_{r-1} operators

While C_1 and C_2 are groups, C_r for $r \geq 3$ is not a group (the product of two C_r gates need not be in C_r). For example, a C_3 gate does not necessarily map a Clifford operator to a Clifford operator. For our purposes what makes the C_r classification useful is that by supplementing the Clifford group generators with a C_3 gate we obtain a universal gate set. Examples of C_3 gates are

Toffoli gate



Controlled phase



" $\pi/8$ gate"

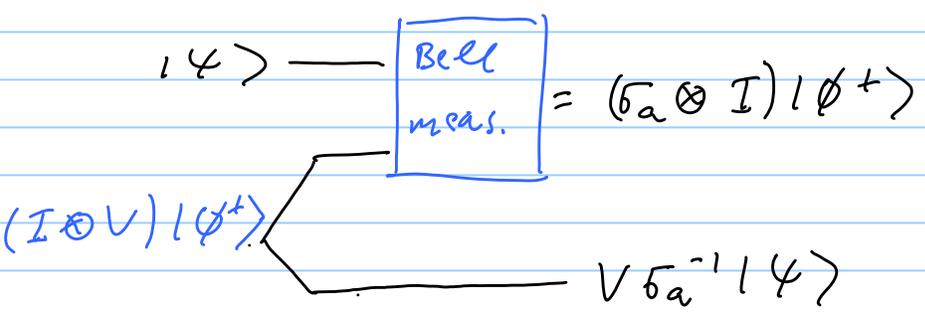


(i.e. \sqrt{P})

The $\pi/8$ gate $T = \begin{pmatrix} 1 & 0 \\ 0 & e^{i\pi/4} \end{pmatrix}$ (despite the name) is actually a $\pi/4$ rotation about the z axis

The second reason we are interested in C_3 gates is that any C_3 gate can be realized using:

- preparation of C_2 operator eigenstates
 - Pauli op. measurement
 - C_2 gates
- } by means of "twisted teleportation"



If Bob applied V^{-1} to his qubit this would be standard teleportation - he would apply σ_a to recover $|\psi\rangle$

But if instead Bob applies $V \sigma_a V^{-1}$, then Bob has $V|\psi\rangle$. If V is a C_r gate, then $V \sigma_a V^{-1}$ is in C_{r-1} . So if Bob can apply C_2 gates, and the state $(I \otimes V)|\phi^+\rangle$ can be prepared reliably, then the C_3 gate V can be executed.

Furthermore, the state $(I \otimes V)|\phi^+\rangle$ is an eigenstate of a C_2 operator - its stabilizer is

$$(I \otimes V)(X \otimes X)(I \otimes V^{-1}) = X \otimes V X V^{-1}$$

$$(I \otimes V)(Z \otimes Z)(I \otimes V^{-1}) = Z \otimes V Z V^{-1}$$

So we can prepare by measuring a C_2 operator.

For some C_3 gates we can simplify this procedure. For example, to apply

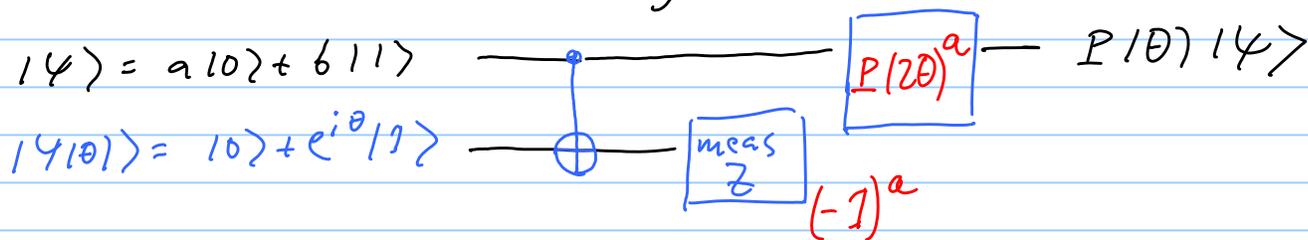
$$P(\theta) = \begin{pmatrix} 1 & 0 \\ 0 & e^{i\theta} \end{pmatrix} \quad (\text{the rotation } \exp(-i\frac{\theta}{2}Z), \text{ up to a phase})$$

it is almost enough to prepare $|\psi(\theta)\rangle = |0\rangle + e^{i\theta}|1\rangle$

This is the $+1$ eigenstate $= P(\theta)(|0\rangle + |1\rangle)$

$$P(\theta) \times P(\theta)^{-1} = \begin{pmatrix} 0 & e^{-i\theta} \\ e^{i\theta} & 0 \end{pmatrix} \quad (\text{ignoring normalization})$$

In this case the following circuit works:



For the measurement outcome $a=0$, the postmeasurement state is $a|0\rangle + e^{i\theta}b|1\rangle = P(\theta)|\psi\rangle$

For outcome $a=1$, it is $e^{i\theta}a|0\rangle + b|1\rangle = e^{i\theta}P(-\theta)|\psi\rangle$

Thus for the $a=0$ outcome the state after the meas. is what we want. For the $a=1$ outcome we can "fix" the state by applying $P(2\theta)$.

In the case of the T gate ($\theta = \pi/4$), $T^2 = P$ is a Clifford gate. We can execute T by preparing $|\alpha_{\pi/4}\rangle = |0\rangle + e^{i\pi/4}|1\rangle$, measuring, and applying P if necessary. The state to be prepared is the eigenstate of the Clifford operator

$$T \times T^{-1} = \begin{pmatrix} 0 & e^{-i\pi/4} \\ e^{i\pi/4} & 0 \end{pmatrix} = \frac{1}{\sqrt{2}}(X+Y).$$

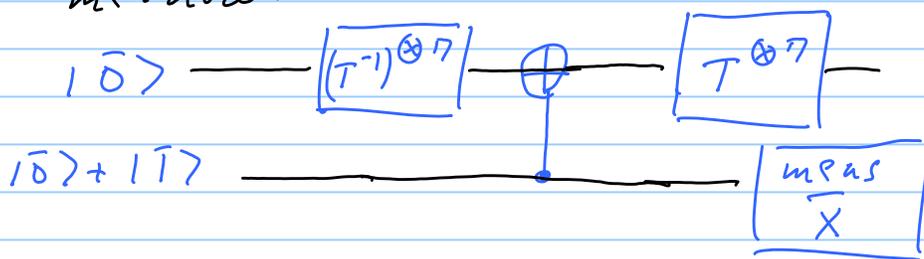
$$\text{Eigenstates are } T|\pm\rangle = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 \\ \pm e^{i\pi/4} \end{pmatrix}$$

Consider the case of the $[[7, 1, 3]]$ code. The Clifford group is transversal, except that $P^{\otimes 7} = (\bar{P})^{-1}$ (the inverse of logical P). So

$$\bar{T} \bar{X} \bar{T}^{-1} = (T^{-1} X T)^{\otimes 7}$$

(H, P generate single-qubit Clifford group where $H^{\otimes 7} = \bar{H}$ and $P^{\otimes 7} = \bar{P}^{\dagger}$, so for any single-qubit Clifford transformation $U^{\otimes 7} = \bar{U}^{\dagger}$.)

We can measure fault tolerantly by the cat state method:



This prepares eigenstate of $\bar{T} \bar{X} \bar{T}^{-1}$ with eigenvalue ± 1 determined by \bar{X} meas of cat state.

To make the meas. fault tolerant, measure twice (followed each time by EC)

and accept only if same outcome twice.



After preparing the ± 1 eigenstate of $T X T^{-1}$, we use it to realize the T gate.